



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Mark S. Moir et al.

Title: OBSTRUCTION-FREE MECHANISM FOR ATOMIC UPDATE OF  
MULTIPLE NON-CONTIGUOUS LOCATIONS IN SHARED MEMORY

Application No.: 10/620,747

Filed: July 16, 2003

Examiner: Unassigned

Group Art Unit: 2125

Atty. Docket No.: 004-8515

September 27, 2004

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- ☒ Form(s) PTO-1449 (2 pages), including copy(ies) of 33 reference(s).  
☐ Other: n/a

to the Examiner's attention in the above-identified application. Citation of such information shall not be construed as:

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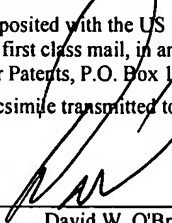
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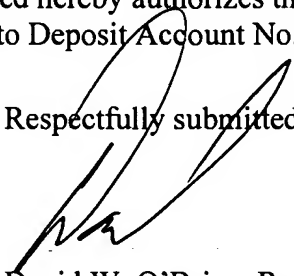
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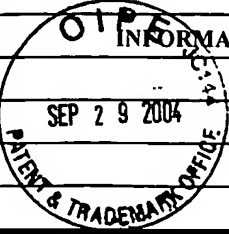
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Respectfully submitted,

  
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U.S. Department of Commerce, Patent and Trademark Office		Attorney Docket No.: 004-8515
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		Applicant(s): Moir et al.
(Use several sheets if necessary)		Filing Date: July 16, 2003
		Group Art Unit: 2125
		Date Submitted: September 27, 2004
<b>NON PATENT LITERATURE DOCUMENTS</b>		
*Examiner Initial	Cite No.	(Including name of author in capital letters, title of article, title of item, date, pertinent pages, volume-issue number(s), publisher, city and/or country where published.)
	1	Afek, Yehuda et al., "Long-Lived Renaming Made Adaptive", 18 <sup>th</sup> Annual ACM Symposium on Principles of Distributed Computing, pages 91-104, 1999.
	2	Afek, Yehuda, "Wait-Free Made Fast", 27 <sup>th</sup> Annual ACM Symposium on Theory of Computing, pages 538-547, 1995.
	3	Agesen, Ole et al., "DCAS-Based Concurrent Deques", 12 <sup>th</sup> Annual ACM Symposium on Parallel Algorithms and Architectures, pages 137-146, July 2000.
	4	Anderson, James H. et al., "Using Local-Spin k-Exclusion Algorithms to Improve Wait-Free Object Implementations", 12 <sup>th</sup> Annual ACM Symposium on Principles of Distributed Computing, November 1995 (revised 1996, 1997).
	5	Arora, Nimar S. et al., "Thread Scheduling for Multiprogrammed Multiprocessors", 10 <sup>th</sup> Annual ACM Symposium on Parallel Algorithms and Architectures, pages 119-129, 1998.
	6	Attiya, Hagit et al., "An Adaptive Collect Algorithm with Applications", Dept. of Computing Science, The Technion, Israel, May 10, 2001.
	7	Barnes, Greg, "A Method for Implementing Lock-Free Shared Data Structures", 5 <sup>th</sup> Annual ACM Symposium on Parallel Algorithms and Architectures, pages 261-270, 1993.
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	11	Dice, David et al., "Mostly Lock-Free Malloc", ACM 2002.ACM SIGPLAN International Symposium on Memory Management, June 2002.
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	14	Herlihy, Maurice, "Dynamic-Sized Lockfree Data Structures", Sun Microsystems Technical Report SMLI TR-2002-112, June 2002.
	15	Herlihy, Maurice et al., "Linearizability: A Correctness Condition for Concurrent Objects", ACM Transactions on Programming Languages and Systems, pages 463-492, July 1990.
	16	Herlihy, Maurice et al., "The Repeat Offender Problem: A Mechanism for Supporting Dynamic-Sized Lock-Free Data Structures", Sun Microsystems Technical Report SMLI TR-2002-112, June 2002.
Examiner		Date Considered
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>		

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		Applicant(s): Moir et al.
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*Examiner Initial	Cite No.	(Including name of author in capital letters, title of article, title of item, date, pertinent pages, volume-issue number(s), publisher, city and/or country where published.)
	17	Herlihy, Maurice et al., "Transactional Memory: Architectural Support for Lock-Free Data Structures", 20 <sup>th</sup> International Symposium in Computer Architecture, 1993.
	18	Herlihy, Maurice et al., "Obstruction-Free Synchronization: Double-Ended Queues as an Example", 23 <sup>rd</sup> International Conference on Distributed Computing, May 2003.
	19	Israeli, Amos et al., "Disjoint-Access-Parallel Implementations of Strong Shared Memory Primitives", 13 <sup>th</sup> Annual ACM Symposium on Principles of Distributed Computing, pages 151-160, 1994
	20	Lamport, Leslie, "How to Make a Multiprocessor Computer that Correctly Executes Multiprocess Programs", IEEE Transactions on Computers, September 1979.
	21	Luchangco, Victor et al., "Nonblocking k-compare-single-swap", 15 <sup>th</sup> Annual ACM Symposium on Parallel Algorithms and Architectures, June 2003.
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	31	Shavit, Nir et al., "Software Transactional Memory", Distributed Computing, Special Issue (10), 1997.
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